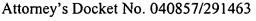
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JC14 C'd PCT/PTO 28 APR 2005

Attn: DO/US



IN THE UNITED STATES DESIGNATED OFFICE (DO/US)

In re: Payman

International Appl. No.: PCT/GB2003/004640

International Filing Date: October 27, 2003

For: A METHOD AND APPARATUS FOR TESTING FOR INTEGRATED CIRCUIT

CONTACT DEFECTS

Mail Stop PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

The patents listed on the attached PTO-1449 were cited in the International Search Report of corresponding International Application No.PCT/GB2003/004640. A copy of the Search Report is enclosed for the Examiner's convenience. In accordance with the Office waiver published July 11, 2003, copies of the cited U.S. patents and patent application publications are not enclosed. Applicant does enclose copies of any cited foreign patent documents and nonpatent literature in accordance with 37 CFR 1.98(a)(2).

The Examiner may wish to consider the notations on the Search Report itself regarding the relevance of each item. It is requested that the Examiner consider these references and officially make them of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. By submitting the listed documents, Applicant in no way makes any admission as to the prior art status of the listed documents, but is instead submitting the listed documents for the sake of full disclosure.

Respectfully submitted,

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Substitute for (Revised 04			Р ТО	Application Number Filing Date				Concurrently herewith			
			ON DISCLOSURE BY APPLICANT	F	First Named Inventor Group Art Unit			Payman			
(Use as many sheets as necessary) Sheet 1 of 1					Examiner Name Attorney Docket Number			040857/291463			
				J. S.	PATENT DO	OCUME	ENTS	<u> </u>			
Examiner Initials*	Cite No.		Document Number mber - Kind Code (if known)		blication Date M-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages of Relevant Figu Appear		Relevant Figures	
	1	US	5-4,779,041		-18-1988	Williamson, Jr.		amson, Jr.			
	2	US	-5,280,237		-18-1994	Buks		Buks			
	3	US	-5,365,180		-15-1994	Edelman				_	
	4	US	5-5,554,928	09			Stringer				
	5	US	3-5,521,513	05	-28-1996	Stringer					
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	9	E	EP 0 571 963 A2		12-01-1993		SPEA S.r.l. Sistemi et al.				
	10	E	EP 0 622 733 A1		11-02-1994	SGS-Thomson Microelectronics s.r.l.			<u> </u>		
				O'	THER DOC	UMENT	rs_	. <u></u>			
Examiner nitials		Cite No.	the item (book, magazine, jo	ame of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue), publisher, city and/or country where published. English Langua Translation Attached						ranslation	
	11		Analog AC Harmonic Method for Detecting Solder Opens, C. Robinson, IEEE Proceedings of the International Test conference, 1997								
Nonlinear Distortion Voltage Testing of Conta- Article, September 26, 1988, pp. 277-280							Mino	wa et al., Conferen	ce Proceedings		
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Signature

Considered

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. #4708773v1